

L Number	Hits	Search Text	DB	Time stamp
1	339	compar\$3 and (read-in with read-out)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:00
2	205647	flash memory and (compar\$3 and (read-in with read-out))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:00
3	1480540	flash memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:00
4	276	(compar\$3 and (read-in with read-out)) and (flash memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:01
5	2555236	failure memory cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:02
6	273	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:03
7	1138349	failure counter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:03
8	141	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:03
9	9780	discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:05
10	0	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:06
11	4015030	discontinuing with test operation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:06
13	4015834	stopping with test operation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:08
14	1887742	(discontinuing with test operation) and (stopping with test operation)	USPAT	2003/08/22 15:09
15	128	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and ((discontinuing with test operation) and (stopping with test operation))	USPAT	2003/08/22 15:09
16	48086	(replacing and (defect\$3 memory with spare memory))	USPAT	2003/08/22 15:16

17	25	(((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and ((discontinuing with test operation) and (stopping with test operation))) and ((replacing and (defect\$3 memory with spare memory)))	USPAT	2003/08/22 15:16
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1	339	compar\$3 and (read-in with read-out)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:00
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4	276	(compar\$3 and (read-in with read-out)) and (flash memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:01
5	2555236	failure memory cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:02
6	273	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:03
7	1138349	failure counter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:03
8	141	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:03
9	9780	discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:05
10	0	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:06
11	4015030	discontinuing with test operation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:06
13	4015834	stopping with test operation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/22 15:08
14	1887742	(discontinuing with test operation) and (stopping with test operation)	USPAT	2003/08/22 15:09
15	128	((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and ((discontinuing with test operation) and (stopping with test operation))	USPAT	2003/08/22 15:09
16	48086	(replacing and (defect\$3 memory with spare memory))	USPAT	2003/08/22 15:16

17	25	(((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and ((discontinuing with test operation) and (stopping with test operation))) and ((replacing and (defect\$3 memory with spare memory)))	USPAT	2003/08/22 15:52
18	746	714/719	USPAT	2003/08/22 15:52
19	416	(714/719).CCLS.	USPAT	2003/08/22 16:37